

D01

BCD 2 Pole 8 Position w/Sep Com to NT Bits	
D I A L	Common C (●) and C (★) connected to terminals indicated
	C1 C2
L	1 2 4
0	★ ★ ★
1	★ ★ ★
2	★ ● ★
3	★ ● ★
4	★ ● ★
5	★ ● ●
6	★ ● ●
7	● ● ●

D02

BCD 2 Pole 10 Position Sep Com to NT Bits	
D I A L	Common C (●) and C (★) connected to terminals indicated
	C1 C2
L	1 2 4 8
0	★ ★ ★ ★
1	★ ★ ★ ★
2	★ ● ★ ★
3	★ ● ★ ★
4	★ ● ★ ★
5	★ ● ★ ●
6	★ ● ★ ●
7	★ ● ★ ●
8	★ ● ★ ●
9	★ ● ★ ●

D03

BCD Dual, 2 Pole 10 Position	
D I A L	Common (C1) and (C2) connected to terminals indicated
	C1 C2
L	1 2 4 8 1 2 4 8
0	
1	●
2	●
3	● ●
4	● ●
5	● ● ●
6	● ● ●
7	● ● ● ●
8	● ● ● ●
9	● ● ● ●

D04

Binary Code w/Complement 2 Pole 16 Position	
D I A L	Common (C) and (\bar{C}) connected to terminals indicated
	C \bar{C}
L	1 2 4 8 $\bar{1}$ $\bar{2}$ $\bar{4}$ $\bar{8}$
0	● ● ● ●
1	● ● ● ●
2	● ● ● ●
3	● ● ● ●
4	● ● ● ●
5	● ● ● ●
6	● ● ● ●
7	● ● ● ●
8	● ● ● ●
9	● ● ● ●
A	● ● ● ●
B	● ● ● ●
C	● ● ● ●
D	● ● ● ●
E	● ● ● ●
F	● ● ● ●

D06

2 Pole Binary with Sep Com to NT Bits	
D I A L	Common C (●) and C (★) connected to terminals indicated
	C1 C2
L	1 2 4 8
0	★ ★ ★ ★
1	★ ★ ★ ★
2	★ ★ ★ ★
3	★ ★ ★ ★
4	★ ★ ★ ★
5	★ ★ ★ ★
6	★ ★ ★ ★
7	★ ★ ★ ★
8	★ ★ ★ ★
9	★ ★ ★ ★
A	★ ★ ★ ★
B	★ ★ ★ ★
C	★ ★ ★ ★
D	★ ★ ★ ★
E	★ ★ ★ ★
F	★ ★ ★ ★

E01

BCD w/Complement 2 Pole 8 Position	
D I A L	Common (C) and (\bar{C}) connected to terminals indicated
	C \bar{C}
L	1 2 4 $\bar{1}$ $\bar{2}$ $\bar{4}$
0	● ● ●
1	● ● ●
2	● ● ●
3	● ● ●
4	● ● ●
5	● ● ●
6	● ● ●
7	● ● ●

E02

BCD w/Complement 2 Pole 10 Position	
D I A L	Common (C) and (\bar{C}) connected to terminals indicated
	C \bar{C}
L	1 2 4 8 $\bar{1}$ $\bar{2}$ $\bar{4}$ $\bar{8}$
0	● ● ● ●
1	● ● ● ●
2	● ● ● ●
3	● ● ● ●
4	● ● ● ●
5	● ● ● ●
6	● ● ● ●
7	● ● ● ●
8	● ● ● ●
9	● ● ● ●

E04

9's Complement BCD, w/Complement 2 Pole 10 Position	
D I A L	Common (C) and (\bar{C}) connected to terminals indicated
	C \bar{C}
L	1 2 4 8 $\bar{1}$ $\bar{2}$ $\bar{4}$ $\bar{8}$
0	● ● ● ●
1	● ● ● ●
2	● ● ● ●
3	● ● ● ●
4	● ● ● ●
5	● ● ● ●
6	● ● ● ●
7	● ● ● ●
8	● ● ● ●
9	● ● ● ●

E05

10's Complement BCD, w/Complement 2 Pole 10 Position	
D I A L	Common (C) and (\bar{C}) connected to terminals indicated
	C \bar{C}
L	1 2 4 8 $\bar{1}$ $\bar{2}$ $\bar{4}$ $\bar{8}$
1	● ● ● ●
2	● ● ● ●
3	● ● ● ●
4	● ● ● ●
5	● ● ● ●
6	● ● ● ●
7	● ● ● ●
8	● ● ● ●
9	● ● ● ●
0	● ● ● ●

E06

Binary Code w/Complement 2 Pole 12 Position	
D I A L	Common (C) and (\bar{C}) connected to terminals indicated
	C \bar{C}
L	1 2 4 8 $\bar{1}$ $\bar{2}$ $\bar{4}$ $\bar{8}$
0	● ● ● ●
1	● ● ● ●
2	● ● ● ●
3	● ● ● ●
4	● ● ● ●
5	● ● ● ●
6	● ● ● ●
7	● ● ● ●
8	● ● ● ●
9	● ● ● ●
10	● ● ● ●
11	● ● ● ●

E07

BCD Excess 3, w/Complement 2 Pole 10 Position	
D I A L	Common (C) and (\bar{C}) connected to terminals indicated
	C \bar{C}
L	1 2 4 8 $\bar{1}$ $\bar{2}$ $\bar{4}$ $\bar{8}$
0	● ● ● ●
1	● ● ● ●
2	● ● ● ●
3	● ● ● ●
4	● ● ● ●
5	● ● ● ●
6	● ● ● ●
7	● ● ● ●
8	● ● ● ●
9	● ● ● ●

E08

Special 4 Pole, BCD w/Complement	
D I A L	Common (C1), (C2), (C3) and (C4) connected to terminals indicated
	C1 C2 C3 C4
L	1 $\bar{1}$ 2 $\bar{2}$ 4 $\bar{4}$ 8 $\bar{8}$
0	● ● ● ●
1	● ● ● ●
2	● ● ● ●
3	● ● ● ●
4	● ● ● ●
5	● ● ● ●
6	● ● ● ●
7	● ● ● ●
8	● ● ● ●
9	● ● ● ●

Truth Tables

F01
BCD and Non-Shorting DECIMAL
2 Pole 10 Position

D I A Common (C1) and (C2) connected to terminals indicated

L	C1				C2									
	1	2	4	8	0	1	2	3	4	5	6	7	8	9
0														
1	●													
2		●												
3	●	●												
4			●											
5	●		●											
6		●	●											
7	●	●												
8				●										
9	●				●									

G01
1-2-2'-4 Berkeley Code
1 Pole 10 Position

D I A Common (C) connected to terminals indicated

L	1	2	2'	4
0				
1	●			
2		●		
3	●	●		
4			●	
5	●	●	●	
6			●	●
7	●	●	●	
8		●	●	●
9	●	●	●	●

G02
1-2-4-2' Berkeley Code
1 Pole 10 Position

D I A Common (C) connected to terminals indicated

L	1	2	4	2'
0				
1	●			
2		●		
3	●	●		
4			●	
5	●	●	●	
6			●	●
7	●	●	●	
8		●	●	●
9	●	●	●	●

H01
1-2-4-2' Berkeley Code w/Complement
1 Pole 10 Position

D I A Common (C) connected to terminals indicated

L	1	2	4	2'	1	2	4	2'
0								
1	●				●			
2		●				●		
3	●	●			●	●		
4			●				●	
5	●	●	●		●	●	●	
6			●	●			●	●
7	●	●	●	●	●	●	●	●
8			●	●			●	●
9	●	●	●	●	●	●	●	●

S01
1 or 2 Pole 2 Throw
Common (C1) and (C2) connected to terminals indicated

DIAL	C1		C2	
	A1	B1	A2	B2
0	0	+	●	●
1	5	1	-	●
2	0	0	+	●
3	5	1	-	●
4	0	0	+	●
5	5	1	-	●
6	0	0	+	●
7	5	1	-	●
8	0	0	+	●
9	5	1	-	●

S03
5-Line TELETYPE
1 Pole 10 Position

D I A Common (C) connected to terminals indicated

L	1	2	3	4	5
0	●	●	●	●	●
1	●	●	●	●	●
2	●	●	●	●	●
3	●	●	●	●	●
4	●	●	●	●	●
5	●	●	●	●	●
6	●	●	●	●	●
7	●	●	●	●	●
8	●	●	●	●	●
9	●	●	●	●	●

S04
5-Line TELETYPE w/even bit parity
1 Pole 10 Position

D I A Common (C) connected to terminals indicated

L	1	2	3	4	5	P
0	●	●	●	●	●	●
1	●	●	●	●	●	●
2	●	●	●	●	●	●
3	●	●	●	●	●	●
4	●	●	●	●	●	●
5	●	●	●	●	●	●
6	●	●	●	●	●	●
7	●	●	●	●	●	●
8	●	●	●	●	●	●
9	●	●	●	●	●	●

S05
BIQUINARY
1 Pole 10 Position

D I A Common (C) connected to terminals indicated

L	ODD	EVEN	0	1	2	3	4	5	6	7	8	9
0		●		●		●		●		●		●
1	●		●		●		●		●		●	
2		●		●		●		●		●		●
3	●		●		●		●		●		●	
4		●		●		●		●		●		●
5	●		●		●		●		●		●	
6		●		●		●		●		●		●
7	●		●		●		●		●		●	
8		●		●		●		●		●		●
9	●		●		●		●		●		●	

S06
NOT/NOR SWITCH 1 Pole 12 Position
(All Positions Shorted except Position indicated on Dial)

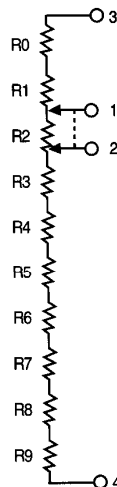
D I A Common (C) connected to terminals indicated

L	0	1	2	3	4	5	6	7	8	9	10	11
0	●	●	●	●	●	●	●	●	●	●	●	●
1	●	●	●	●	●	●	●	●	●	●	●	●
2	●	●	●	●	●	●	●	●	●	●	●	●
3	●	●	●	●	●	●	●	●	●	●	●	●
4	●	●	●	●	●	●	●	●	●	●	●	●
5	●	●	●	●	●	●	●	●	●	●	●	●
6	●	●	●	●	●	●	●	●	●	●	●	●
7	●	●	●	●	●	●	●	●	●	●	●	●
8	●	●	●	●	●	●	●	●	●	●	●	●
9	●	●	●	●	●	●	●	●	●	●	●	●
10	●	●	●	●	●	●	●	●	●	●	●	●
11	●	●	●	●	●	●	●	●	●	●	●	●

S07
INCREMENTAL Voltage Divider
2 Pole 10 Position

D I A Pins 1 and 2 connected across resistor indicated

L	R0	R1	R2	R3	R4	R5	R6	R7	R8	R9
0	●									
1		●								
2			●							
3				●						
4					●					
5						●				
6							●			
7								●		
8									●	
9										●



SCHEMATIC
Dial set at
Position no. 2

S08
WOLFF POGGENDORF Voltage Divider
10 Position

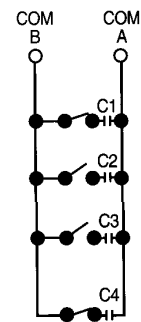
D I A Resistors in circuit

L	1	2	4	2'	1	2	4	2'	R9
0									●
1	●	●	●	●					●
2		●	●	●	●				●
3			●	●	●	●			●
4	●	●	●	●	●	●			●
5			●	●	●	●	●		●
6	●	●	●	●	●	●	●		●
7			●	●	●	●	●	●	●
8	●	●	●	●	●	●	●	●	●
9			●	●	●	●	●	●	●

S09
CAPACITOR DECADE
1-2-3-4 Code 10 Position

D I A Capacitors in Circuit

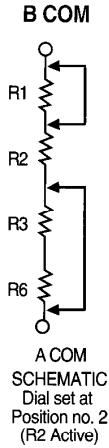
L	C1	C2	C3	C4
0				
1	●			
2		●		
3			●	
4				●
5	●	●	●	●
6	●	●	●	●
7	●	●	●	●
8	●	●	●	●
9	●	●	●	●



SCHEMATIC
Dial set at Position no. 5
(C1 and C4 Active)

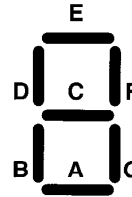
S10
RESISTOR DECADE
1-2-3-6 Code 10 Position

DIAL	Resistors in circuit					
L	R1	R2	R3	R6		
0	●					
1		●				
2			●			
3				●		
4	●					
5		●				
6			●			
7	●					
8		●				
9			●			



S11
7 BAR INDICATOR
1 Pole 10 Position

DIAL	Common connected to terminals indicated						
L	A	B	C	D	E	F	G
0	●	●		●	●	●	●
1	●	●		●	●		
2	●	●		●	●		
3	●	●		●	●		
4		●	●	●	●		
5	●	●		●	●		
6	●	●		●	●		
7		●	●	●	●		
8	●	●		●	●		
9	●	●		●	●		



S12
KELVIN VARLEY: Voltage Divider
10 Position

DIAL	Pins 1 and 2 connected across resistors indicated										
L	R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	R10
0	●	●									
1	●		●								
2		●		●							
3			●		●						
4				●		●					
5					●		●				
6						●		●			
7							●		●		
8								●		●	
9									●		●

S15
Modified 5 Bit
TELETYPE
10 Position

DIAL	Common (C) connected to terminals indicated				
L	1	2	3	4	5
0		●	●	●	●
1		●	●	●	●
2	●				●
3	●				●
4	●	●		●	
5	●				●
6	●				●
7	●	●		●	
8	●				●
9					●

S17
Resistor DECADE
1-2-4-2' Code
10 Position

DIAL	Resistors in circuit			
L	R1	R2	R4	R2'
0				
1	●			
2	●	●		
3	●	●		
4			●	
5	●	●		
6	●	●		
7	●	●		
8	●	●		
9	●	●		

S18
Resistor DECADE
1-2-2-2-2 Code
10 Position

DIAL	Resistors in circuit				
L	R1	R2	R3	R4	R5
0					
1	●				
2					●
3	●				
4					●
5	●				
6					●
7	●				
8					●
9	●				

S19
2 Pole 2 Throw

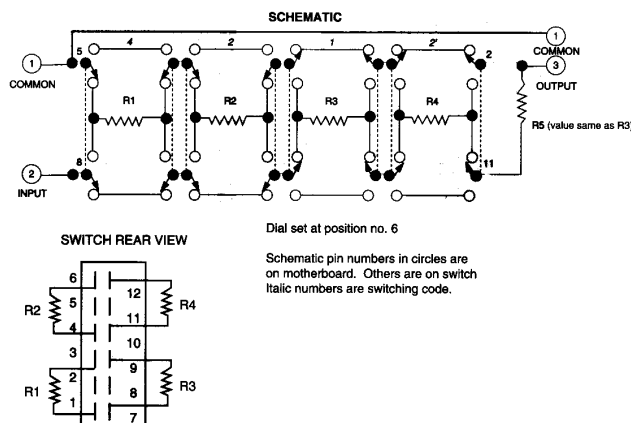
Commons C(●) and C(*) connected to terminals indicated		
DIAL	A	B
0	0 + ● ★	●
1	5 - * ●	●
2	0 0 + ● ★	●
3	1 5 - * ●	●
4	0 0 + ● ★	●
5	1 5 - * ●	●
6	0 0 + ● ★	●
7	1 5 - * ●	●
8	0 0 + ● ★	●
9	1 5 - * ●	●

S20
2 Pole 1-1'-2-5 with Complement

DIAL	Commons (C) and (C') connected to terminals indicated						
L	5	2	1'	1	5	2	1'
0				●	●	●	●
1				●	●	●	●
2	●			●	●	●	●
3	●			●	●	●	●
4	●			●	●	●	●
5				●	●	●	●
6	●			●	●	●	●
7	●			●	●	●	●
8	●			●	●	●	●
9	●			●	●	●	●

S21
EECO Voltage Divider
(THUMB POT) 10 Position

DIAL	Resistors in circuit between term 2 & 5				Resistors in circuit between term 8 & 11			
L	4	2	1	2'	4	2	1	2'
	R1	R2	R3	R4	R1	R2	R3	R4
0					●	●	●	●
1					●	●	●	●
2	●				●	●	●	●
3					●	●	●	●
4	●				●	●	●	●
5	●				●	●	●	●
6	●				●	●	●	●
7	●				●	●	●	●
8	●				●	●	●	●
9	●				●	●	●	●



S22
1-2-4-5 Code

DIAL	Common (C) connected to terminals indicated				
L	1	2	4	5	
0					
1	●				
2	●				
3	●				
4					
5					
6	●				
7					
8	●				
9	●				

S23
JOHNSON CODE

DIAL	Common (C) connected to terminals indicated				
L	A	B	C	D	E
0	●	●	●	●	●
1	●	●	●	●	●
2	●	●	●	●	●
3	●	●	●	●	●
4					
5					
6	●	●	●	●	●
7	●	●	●	●	●
8	●	●	●	●	●
9	●	●	●	●	●

Consult factory for additional information.